

FIG. 1 is a block diagram of a computer system 100. The computer system 100 includes a processor 102, a memory 104, a read only memory 106, a mass storage device 107, a display 121, a keyboard 122, a cursor control device 123, and a communication device 125. The processor 102 is connected to the memory 104, the read only memory 106, and the mass storage device 107 via a bus 101. The display 121, keyboard 122, cursor control device 123, and communication device 125 are also connected to the bus 101. The computer system 100 is connected to a network 110.

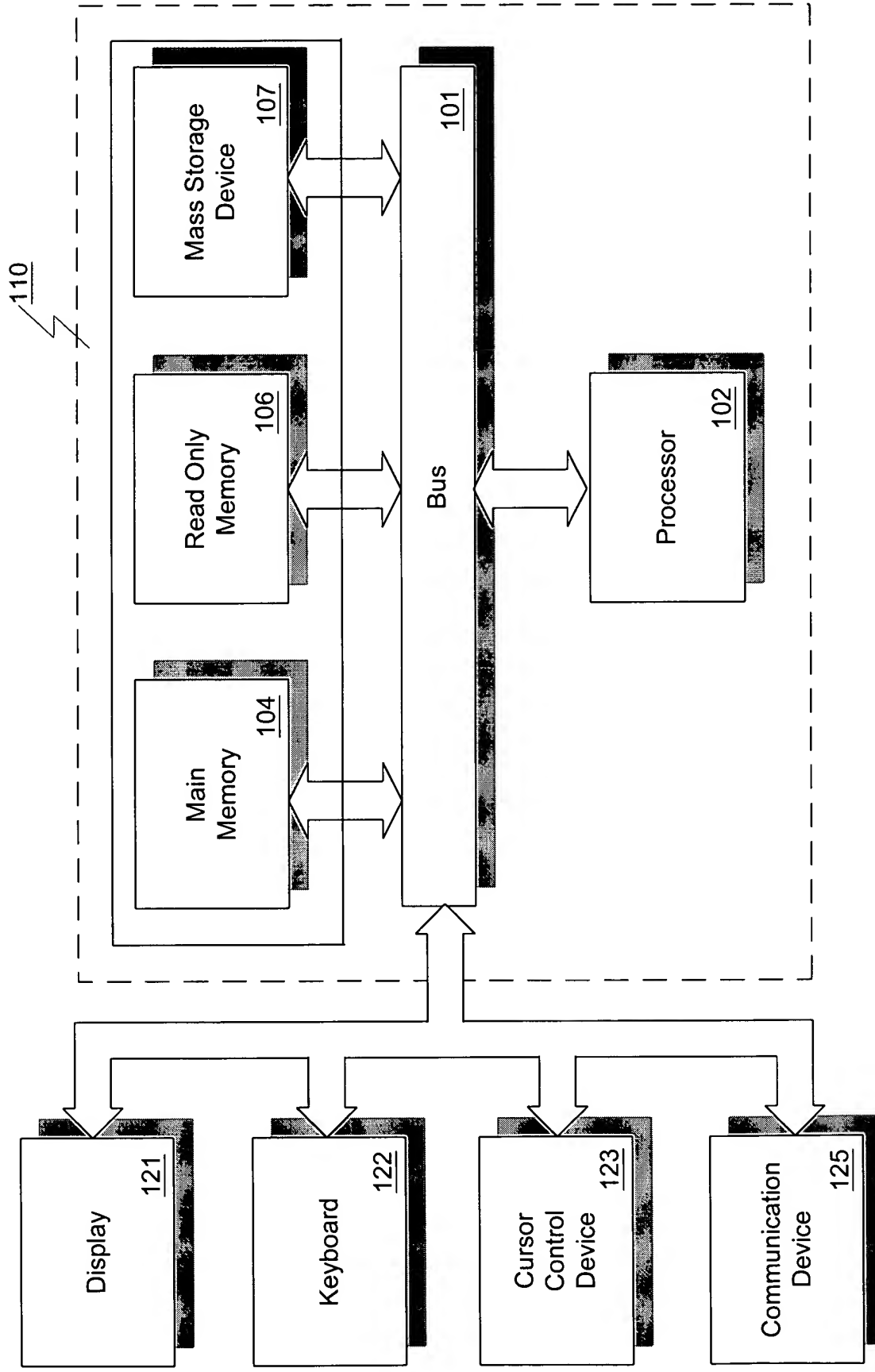


Figure 1

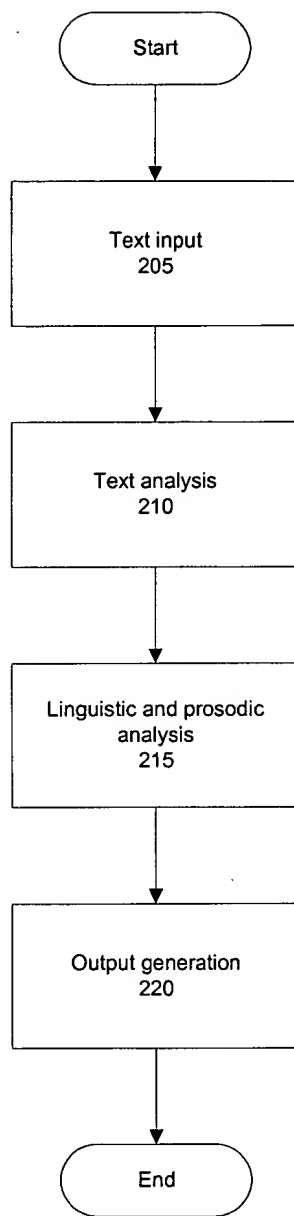


Figure 2

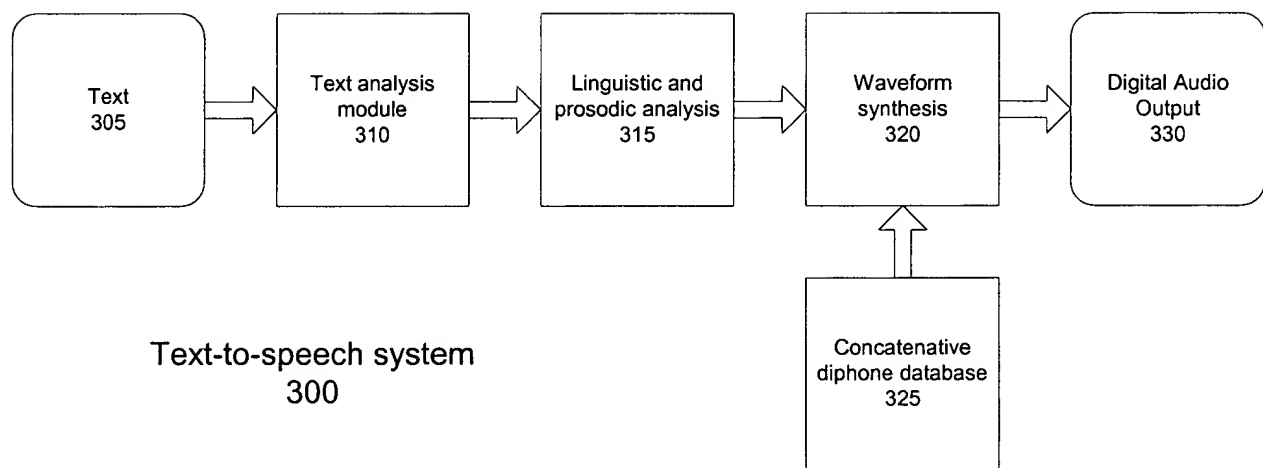


Figure 3

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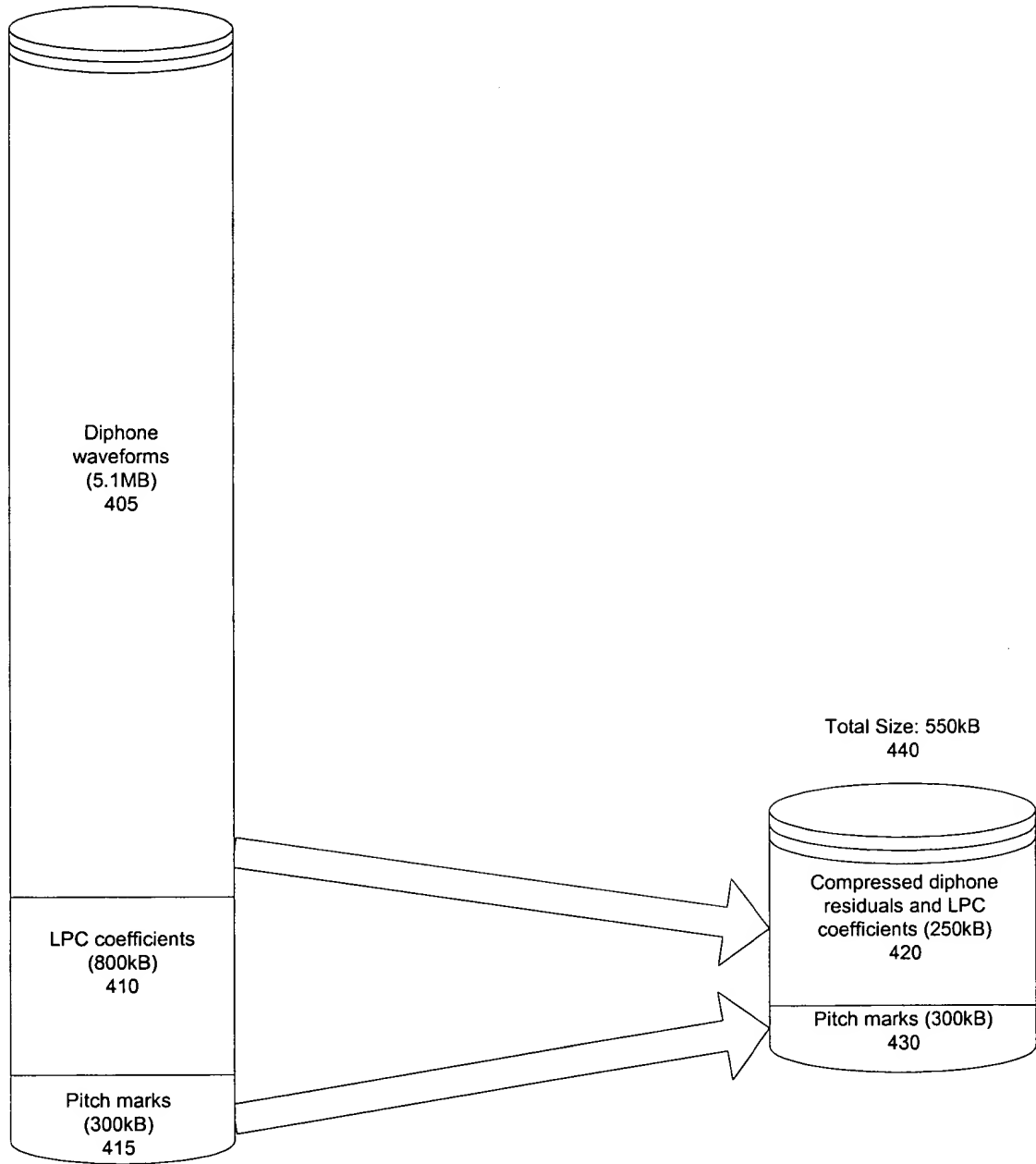


Figure 4

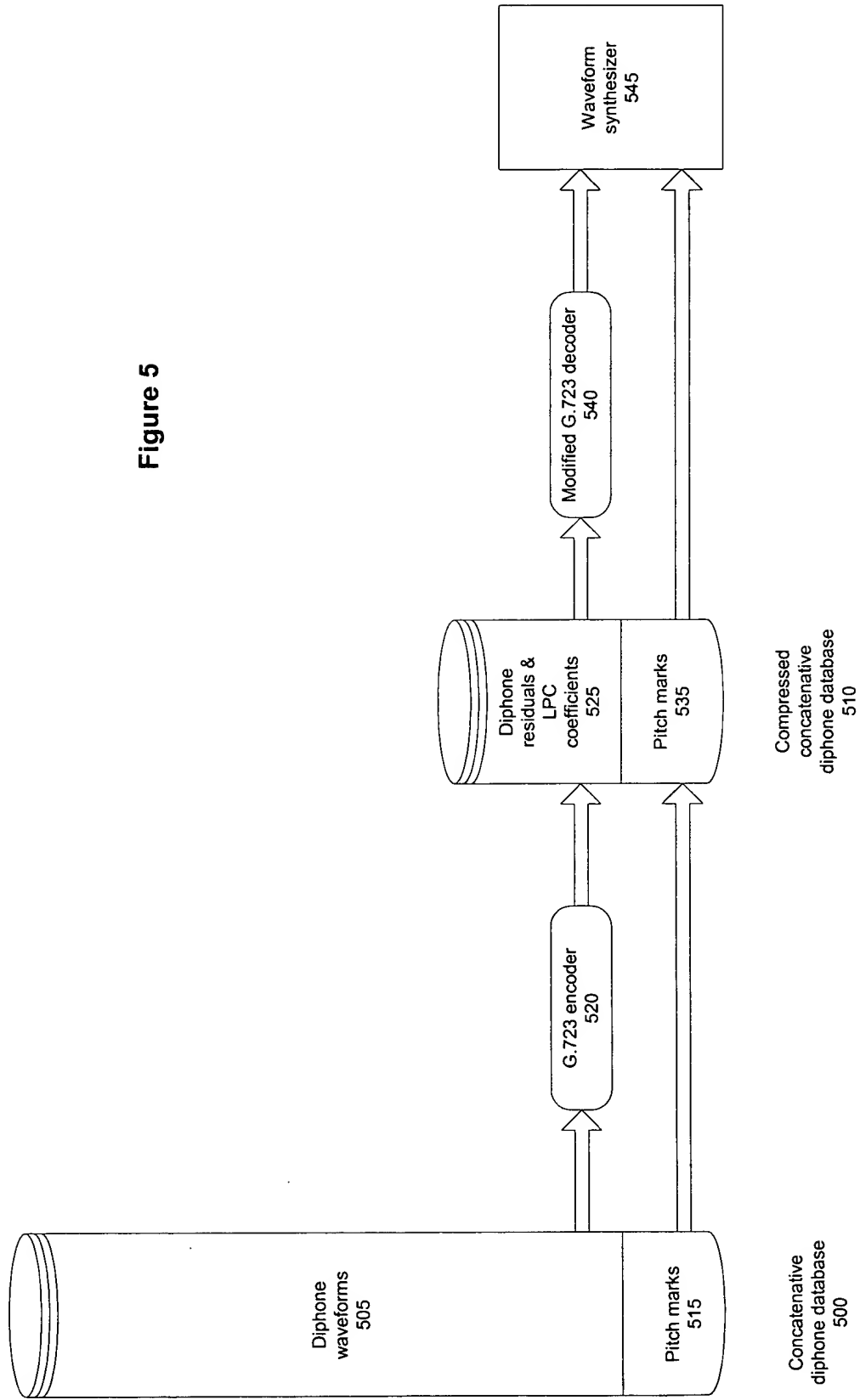


Figure 5

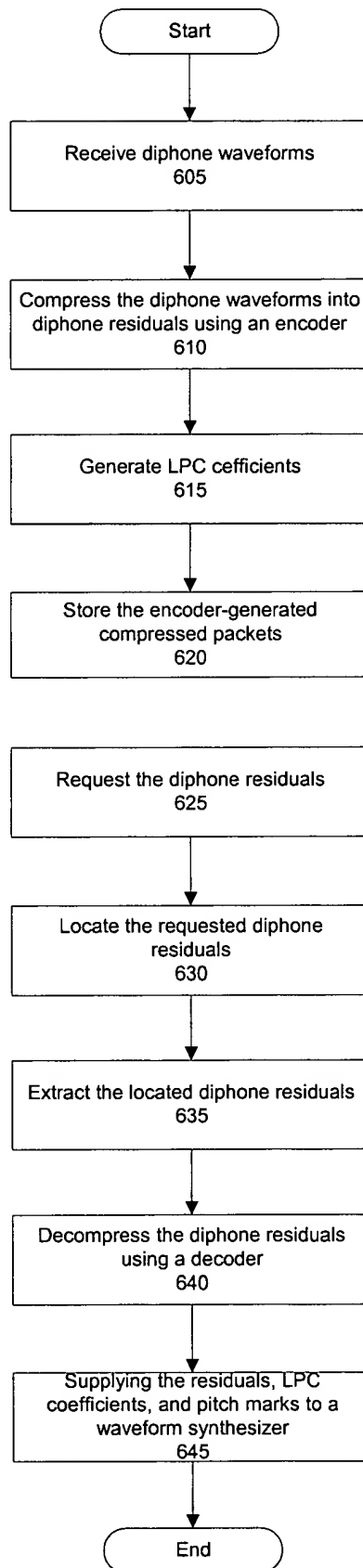


Figure 6

FIG. 7 is a block diagram of a system architecture for a web-enabled handheld device. The system includes a Web-enabled handheld device 725, a Wireless ISP 720, an Internet 715, a Customized speech database 705, a Compressed database 735, a Waveform synthesizer 740, and an Audio decoder 745. The Web-enabled handheld device 725 is connected to the Wireless ISP 720 via a network interface. The Web-enabled handheld device 725 contains a Web Interface 730, a Compressed database 735, a Waveform synthesizer 740, and an Audio decoder 745. The Web Interface 730 is connected to the Compressed database 735. The Compressed database 735 is connected to the Waveform synthesizer 740. The Audio decoder 745 is connected to the Waveform synthesizer 740. The Wireless ISP 720 is connected to the Internet 715. The Internet 715 is connected to the Customized speech database 705 via a network interface 710.

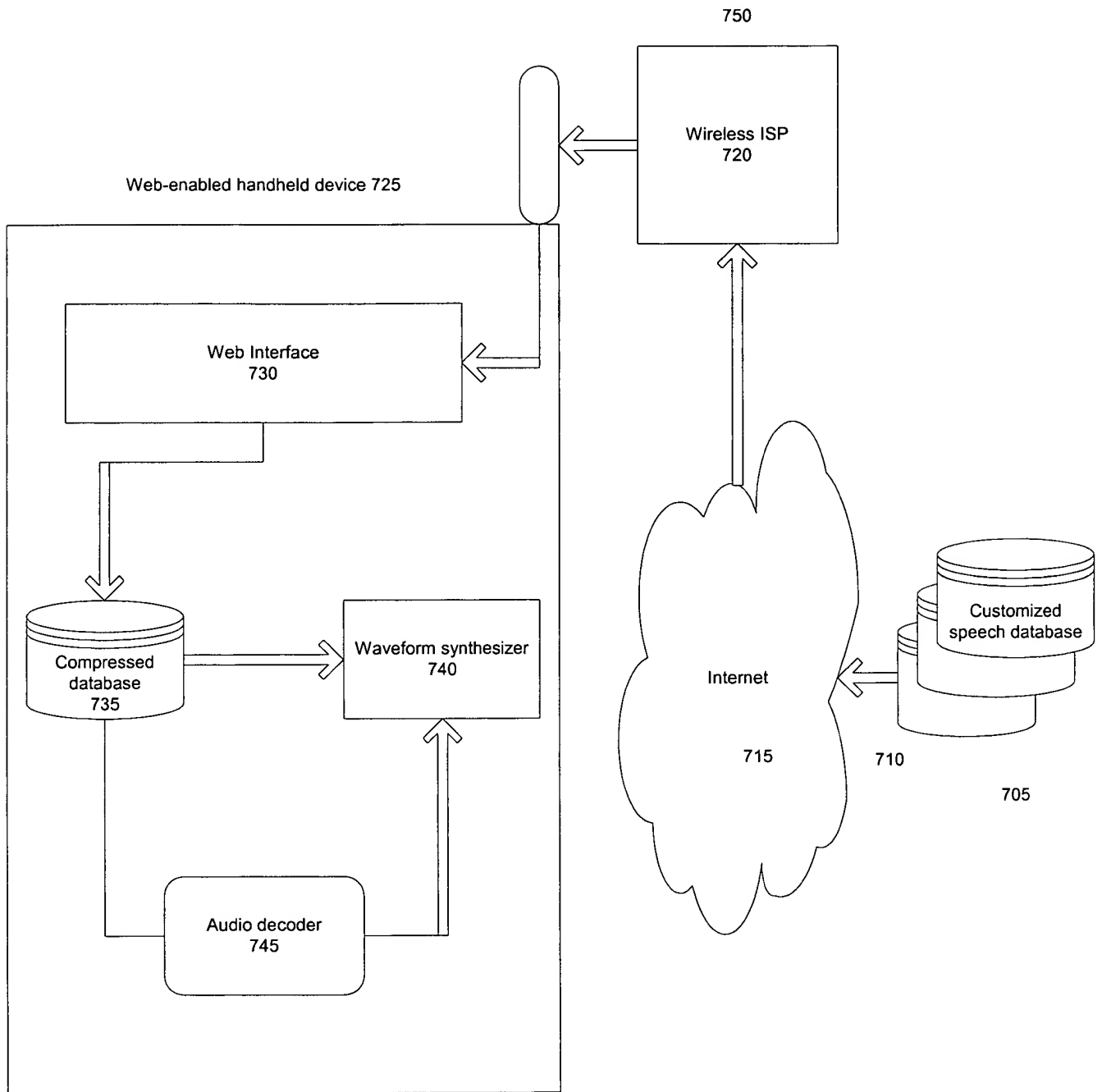


Figure 7